

MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

(An UGC Autonomous Institution. Affiliated to JNTUH, Hyderabad Accredited 2nd time by NAAC with 'A' Grade & NBA and Recipient of World Bank Assistance under TEQIP--II S.C. 1.1)

Maisammaguda (H), Medchal-Malkajgiri District, Secunderabad,

Telangana State — 500100

www.mrec.ac.in

Department of Electronics and Communication Engineering

CIRCULAR

Date: 04/06/2015

All the students are hereby informed that Value Added Course on "VLSI Semi Custom Design" on 18th to 23rd, June 2015 is being organized by the Electronics and Communication Engineering department. The resource person for the course is "SHARATH KANTH, ENTUPLE TECHNOLGIES PVT LTD, BANGALORE".

Students are advised to register their names to the programme coordinator "Mrs C. Silpa", on or before 14thJune 2015 and utilize this opportunity to enhance their skills by attending the programme.

The detailed schedule of the programme will be displayed in the notice board.

Head of the Department

Copy to:

- 1) Circulation in Students classroom
- 2) All HOD's
- 3) Notice Boards
- 4) PA to principal for filing.

PRINCIPAL
Malla Reddy Engineering College
(Autonomous)
Maisammaguda, Dhulapally,
(Post Via Kompally), Sec'bad-500 100



Target Group:

The Workshop addresses various concepts related to engineering principles real life applications and the integration of different concepts. Students will be trained in designing and developing the applications, starting from the basics and implementing the applications. Ultimately, this will change the way students view the technology and what they can do with it.

Registration:

Details	Amount		
Fee	free		

Candidates can register by sending the Registration form attached to this brochure.

Transportation:

Participants can avail the Institute bus facility from Different locations of the twin cities.

Important Date:

Last Date for submission of Registration form by Post/email:16 th June **2015.**

Registration form can be downloaded from: www.mrec.ac.in

Address for Correspondence

Mrs C. Silpa,

Assoc.Professor, ECE Department

Malla Reddy Engineering College (Autonomous)

Maisammaguda, Dhulapally Post,

Via Kompally, Secunderabad-500100

Emailcsilpa@mrec.ac.in Phone: 9652595344

Organizing Committee

Chief Patron:
Sri Ch. Malla Reddy, MP
Founder Chairman, CMR Educational Society

Patrons:

Sri Ch. Mahender ReddySecretary, CMR Educational Society

Dr. Ch. Bhadra Reddy Secretary, MRIMS

Co-Patron:

Dr. S. Sudhakara Reddy, Principal, MREC (A)

Convener:

Mr.M.J.C.Prasada, Professor & Head, ECE dept.

Co-Ordinators

Mrs C. Silpa, Assoc. Professor, ECE:
Dr. A .Pradeep Kumar Assoc. Professor, ECE

Co-Coordinators:

Mrs P. Sowjanya, Assoc. Professor, ECE
Mrs G.B. Subhashini, Assoc. Professor, ECE

Members:

K Rajendra Prasad, Professor, ECE
Dr G S K Gayatri Devi, Assoc. Professor, ECE Dept
N.Pandu Ranga Reddy, Assoc. Professor, ECE
Dr A Pradeep Kumar Assoc. Professor, ECE
Dr .T.Srinivas Reddy, Assoc. Professor, ECE
Mrs P. Sowjanya, Assoc. Professor, ECE
Mr.CH.Bhanu Prakash, Assoc. Professor

Malla Reddy Engineering College (Autonomous)

Maisammaguda, Dhulapa ly (Post Via Kompally), Sec'bad-5, 0, 00 One week Work shop On

"VLSI Semi Custom Design "
From 18th to 23rd June 2015



Coordinator
Mrs C. Silpa,
Assoc. Professor, ECE

Convener

Mr.M J. CJ Prasad Professor & Head, ECE Department



Organized by
Department of Electronics and
Communication Engineering (ECE)

Malla Reddy Engineering College (Autonomous)

Maisammaguda, Secunderabad – 500 100 Phone: 040-65864982, 040-64634234. Fax: 040-23090485

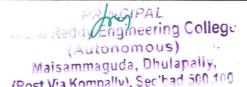
MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

List of Students Registered for Value Added Course

2015-16

				Year/		DATE OF	DATE OF
S.No	Branch	Roll Number	Student Name	Sem	Value Added Course	INTRODUCTION	COMPLETION
1	ECE	12J41A0401	A SOMESHWAR REDDY	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
2	ECE	12J41A0402	ADDU JAMES STEEVEN	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
3	ECE	12J41A0404	AERUPULA SRIKANTH	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
4	ECE	12J41A0406	AKKARAPU PRIYANKA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
5	ECE	12J41A0408	BIJJIGA SRUJAN	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
6	ECE	12J41A0410	K AMULYA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
7	ECE	12J41A0419	DODDA ARAVIND	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
8	ECE	12J41A0421	DURGAM RAVITEJA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
9	ECE	12J41A0424	GUNJALA PAVANI	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
10	ECE	12J41A0425	HARSH KEDIA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
11	ECE	12J41A0427	JADI AKHILA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
12	ECE	12J41A0430	KAKURALA SWAMY	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
13	ECE	12J41A0431	KANTEPUDI AKHIL	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
14	ECE	12J41A0433	KOMANDLA SWETHA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
15	ECE	12J41A0437	M S SHINY SUPRIYA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
16	ECE	12J41A0438	MABBU SIREESHA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
17	ECE	12J41A0445	N SANDEEP	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
18	ECE	12J41A0446	NORU CHAITANYA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
19	ECE	12J41A0447	POLAVENI RAMESH	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
20	ECE	12J41A0456	TRIPTI RANJAN	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015





21	ECE	12J41A0461	AKULA SINDHUJA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
22	ECE	12J41A0462	ATHELI AISHWARYA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
23	ECE	12J41A0463	B KISHORE	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
24	ECE	12J41A0464	BODA SHIRISHA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
25	ECE	12J41A0479	K RAHUL	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
26	ECE	12J41A0480	K SUMANTH KUMAR	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
27	ECE	12J41A0482	KOTI SRUTHI	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
28	ECE	12J41A0496	PENDYALA HARSHINI	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
29	ECE	12J41A0497	POLA MANASA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
30	ECE	12J41A04A0	RAJOJU NARESH	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
31	ECE	12J41A04A2	ROUTHU BHUMESH	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
32	ECE	12J41A04B1	V KALPANA NAIK	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
33	ECE	12J41A04B2	VADDIPARTHY NIKITA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
34	ECE	12J41A04B9	YEDLA SOUMYA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
35	ECE	12J41A04C0	YEMINEDI SAKETH	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015
36	ECE	13J45A0418	VANGALA ROJA	IV/I	VLSI Semi Custom Design	18.06.2015	23.06.2015

HOD ECE

Principal

(Autonomous)

Maisammaguda, Dhulapally,

(Post Via Kompally), Sec'bad-500 100







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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr A SOMESHWAR REDDY bearing Roll Number 12J41A0401 has successfully completed Value added Course in "VLSI Semi Custom Design" Organized by the Department of Electronics and Communication Engineering from 18.06.2015 to 23.06.2015

Coordinator

HOD-ECE

Principal
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This is to certify that Mrs./Ms./Mr ADDU JAMES STEEVEN bearing Roll Number 12J41A0402 has successfully completed Value added Course in "VLSI Semi Custom Design" Organized by the Department of Electronics and Communication Engineering from 18.06.2015 to 23.06.2015

Coordinator 500 100

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This is to certify that Mrs./Ms./Mr AERUPULA SRIKANTH bearing Roll Number 12J41A0404 has successfully completed Value added Course in "VLSI Semi Custom Design" Organized by the Department of Electronics and Communication Engineering from 18.06.2015 to 23.06.2015

Coordinator

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr AKKARAPU PRIYANKA bearing Roll Number 12J41A0406 has successfully completed Value added Course in "VLSI Semi Custom Design" Organized by the Department of Electronics and Communication Engineering from 18.06.2015 to 23.06.2015

Coordinator

HOD-ECE

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Maisammaguda, Dhulapally, Post Via Kompally), Sec'bad-500 100







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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr BIJIIGA SRUJAN bearing Roll Number 12J41A0408 has successfully completed Value added Course in "VLSI Semi Custom Design" Organized by the Department of Electronics and Communication Engineering from 18.06.2015 to 23.06.2015

Coordinator

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Principal
PRINCIPAL
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Maisammaguda, Dhulapally.

(Post Via Kompally), Sec'bad-50







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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING <u>Certificate of Completion</u>

This is to certify that Mrs./Ms./Mr K AMULYA bearing Roll Number 12J41A0410 has successfully completed Value added Course in "VLSI Semi Custom Design" Organized by the Department of Electronics and Communication Engineering from 18.06.2015 to 23.06.2015

Coordinator

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Malla Reddy Engineering Collect
(Autonomous)

Maisammaguda, Dhulapally

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr DODDA ARAVIND bearing Roll Number 12J41A0419 has successfully completed Value added Course in "VLSI Semi Custom Design" Organized by the Department of Electronics and Communication Engineering from 18.06.2015 to 23.06.2015

Coordinator

HOD-ECE

Malla Reddy Engineering College (Autonomous) Maisammaguda, Dhulapally

REPORT ON WEEK Work Shop on "VLSI Semi Custom Design"

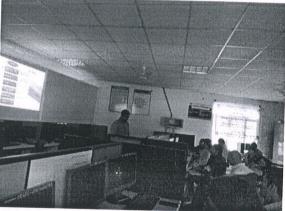
The department of ECE has organized **three day** Faculty Development Program (FDP) on "<u>VLSI Semi Custom Design</u>" during **18.06.2015** to **23.06.2015** at **VLSI lab** in ECE department from 9.30 A.M to 4.30 P.M.The department has invited the resource person **Mr.Sharath Kanth**, Senior Application Engineer, **Entuple Technologies Pvt. Ltd.**, **Bangalore**.

The workshop aims to provide analog and digital IC design using Cadence tool. The Resource person explained about full and semi custom IC design flow and discussed which part of Cadence tool is used in each level of design. He explained analog full custom design and shown practical way of analysis step by step with simulation and layout design.

They given clear analysis with physical verification using DRC and LVS checks and parasitic extraction using XRC tool. On third day, introduced ASIC design flow with example of design called "counter". He discussed with step by step design with RTL compiler, encounter and physical design with different steps like floor planning, routing, timing analysis and physical verification.

Students actively participated in this workshop and this six day workshop was coordinated by Mrs C Silpa





(C Silpa) CO-ORDINATOR

(Dr.M J C Prasad) HEAD, ECE DEPARTMENT (Dr.S.Sudhakara Reddy) PRINCIPAL

Sol 100 P

Malla Reddy Engineering College (Autonomous)

Maisammaguda, Dhulapally, (Post Via Kompally), Sec'bad-500 100

PRINCIPAL